Listing of the Claims

Claims 1-11 (Cancelled).

12. (Currently Amended) A display device comprising a plurality of pixels, each pixel in the display comprising:

a serial shifter that accepts a serial bit stream and has an n-bit wide output;

an n-bit wide data latch that latches data received from the output of the serial shifter; wherein each pixel comprises:

a plurality of optical parts

a data latch corresponding to each optical part;

a digital to analog converter for each of said plurality of optical parts to which output of the respective data latch is applied[[;]], ~~and~~wherein each~~an~~ optical part is driven by the digital to analog converter; and

a serial shifter corresponding to each optical part.

13. (Cancelled).

14. (Cancelled).

15. (Currently Amended) The display device of claim [[14]]12, wherein said serial shifters in each pixel are arranged to receive data in parallel.

16. (Currently Amended) ~~The display device of claim 13,~~ A display device comprising a plurality of pixels, each pixel in the display comprising:

a serial shifter that accepts a serial bit stream and has an n-bit wide output;

an n-bit wide data latch that latches data received from the output of the serial shifter;

a digital to analog converter to which output of the data latch is applied; and

an optical part driven by the digital to analog converter, wherein each pixel includes a plurality of optical parts, and wherein each pixel comprises:

_____a serial shifter corresponding to each optical part;

_____a data latch corresponding to each optical part;

_____a single digital to analog converter;

_____a first switch to selectively and individually apply the output of the pixel's data latches to the single digital to analog converter; and

_____a second switch to selectively and individually apply the output of the single analog to digital converter to the plurality of optical parts.

17. (Original) The display device of claim 16, wherein serial shifters in each pixel are arranged to receive data in parallel.

18. (Original) The display device of claim 12, wherein groups in the plurality of pixels comprise interconnected serial shifters to serially receive a data set.

19. (Original) The display device of claim 18, further comprising a global clock line to control shifting of data through interconnected serial shifters of groups of pixels in the plurality of pixels.

20. (Original) The display device of claim 19, further comprising a global load line to control latching of data by data latches in the plurality of pixels.

21. (Original) The display device of claim 12, wherein said optical part comprises a light emitter.

22. (Original) The display device of claim 21 wherein said light emitter comprises a light emitting diode.

23. (Original) The display device of claim 22 wherein said light emitter comprises an organic light emitting diode.

24. (Original) The display device of claim 12 wherein said optical part comprises a reflector.

25. (Original) The display device of claim 24 wherein said reflector comprises a digital micro-mirror.

26. (Original) The display device of claim 25 wherein said reflector comprises a diffractive light device.

27. (Original) The display device of claim 12, wherein outputs of data latches in the plurality of pixels are applied simultaneously to their analog to digital converters in accordance with a global load signal.

Claims 28-31 (Cancelled).